

ABSTRACT OF THE DISCLOSURE

A plasma display panel includes first and second substrates spaced apart from each other at a distance while proceeding substantially parallel to each other. The first and the second substrates have a display area and a non-display area. A plurality of address electrodes are formed on the first substrate, and covered by a dielectric layer. Main barrier ribs are arranged between the substrates to form discharge cells. Phosphor layer is formed with the discharge cells. A plurality of discharge sustain electrodes are formed on the surface of the second substrate facing the first substrate, and covered by a dielectric layer. Reinforcing barrier ribs are arranged at the non-display area while surrounding the display area, and connected to the main barrier ribs with an outer structure curved toward the outside of the substrates.

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